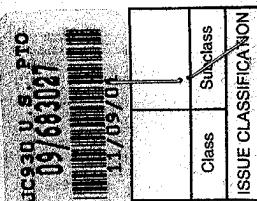


Jc943



U.S. UTILITY Patent Application

	O.I.P.E.	STTH	PATENT DATE
SCANNED 6 M 1	Q.A.		

APPLICATION NO. 09/683027	CONT/PRIOR 307 257	CLASS 307 724	SUBCLASS	ART UNIT 2805 2807	EXAMINER JH JH
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APPlicants

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TITLE

Dual chip stack method for electro-static discharge protection of integrated circuits

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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